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**Message:**

In re Application

Inventor(s): Stephen E. Savas  
Application No.: 09/692,255  
Filed: October 18, 2000

Art Unit: 1746  
Examiner: MARKOFF, Alexander

Title    Systems And Methods For Photoresist Strip  
And Residue Treatments In Integrated Circuit  
Manufacturing

PLEASE SEE ATTACHED ISSUE FEE TRANSMITTAL.

Ref: 14912.777

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24024RR 4 DDC/BBT

PAGE 1/2 \* RCVD AT 7/12/2004 3:08:43 PM [Eastern Daylight Time] \* SVR:USPTO-EFXRF-1/4 \* DNIS:8729306 \* CSID:650 493 6811 \* DURATION (mm:ss):01:00

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Linda Faye	(Depositor's name)
<i>Linda Faye</i>	(Signature)
July 12, 2004	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/692,255	10/19/2000	Stephen E. Savas	14912.777	6821

TITLE OF INVENTION: SYSTEMS AND METHODS FOR PHOTORESIST STRIP AND RESIDUE TREATMENT IN INTEGRATED CIRCUIT MANUFACTURING

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	07/12/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
MARKOFF, ALEXANDER	1746	134-001300

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